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The Sigrity SPEED2000 technology is designed to work with popular chip/package/board design flows. The tool lets you perform time-domain analysis to confirm that designs meet specified targets, it understands complex voltage noise (SSN), and helps you identify improvement opportunities.

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• An integrated foundation for Sigrity technologies - Launching of Sigrity products from layout environment, automatically passing layout data and setup to Sigrity individual products • PI Experts may change the design, re-analyze with advance Sigrity PI tools, and save a local copy of improved design Main Functions:

Cadence Sigrity Simulation | FlowCAD Cadence® Sigrity™ SPEED2000™ technology provides for direct layout-based, time-domain simulations of an entire board design or for a specific IC package together with the PCB. These simulations can include various SPICE/S-parameter interconnect models and component models

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